



S/N 10/613,182

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: OTA et al. Examiner: Chang, Joseph
Serial No.: 10/613,182 Group Art Unit: 2817
Filed: July 3, 2003 Docket No.: 10873.1246US01
Title: BIPOLAR TRANSISTOR FOR VOLTAGE CONTROLLED OSCILLATOR
USING A CAPACITANCE ADJUSTMENT LINE

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 23, 2005.

By: 

Name: Antonette Peters

COMMUNICATION REGARDING PRIOR ART (NEW REFERENCES)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

Applicant hereby encloses the Japanese Office Action citing JP 9-260396A and its English abstract, JP 3-64065 A and its English abstract, JP 10-214937 A and its English abstract. Applicant respectfully requests that the enclosed references be placed in the file wrapper of the above-referenced application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller (Reg. No. 30,300), at (612) 455.3804.

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PATENT TRADEMARK OFFICE

Respectfully submitted,

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Dated: August 23, 2005